Preliminary ECS154A Class Outline

Lecture	Topic	Chapter	Date
1	Introduction		September 27
2	History & Review	Chapter 1	September 30
Disc	*Digital Design Tools*		September 30
3	Digital Design (Boolean Logic)	Appendix A	October 2
4	Digital Design (Gates)	Appendix A	October 4
5	Digital Design (Combinational)	Appendix A	October 7
Disc	*More Design Tools*		October 7
6	Digital Design (Combinational)	Appendix A	October 9
7	Digital Design (Combinational)	Appendix A	October 11
8	Digital Design (Sequential)	Appendix A	October 14
Disc			October 14
9	Digital Design (Sequential)	Appendix A	October 16
10	Digital Design (Sequential)	Appendix A	October 18
11	I/O	Chapter 4	October 21
Disc	*The SPIM Simulator*		October 21
12	I/O	Chapter 4	October 23
13	I/O	Chapter 4	October 25
14	I/O	Chapter 4	October 28
Disc	*Midterm Review*		October 28
15	Midterm		October 30
16	I/O Interconnect Structures	Chapter 4	November 1
17	I/O Interconnect Structures	Chapter 4	November 4
Disc			November 4
18	I/O Interconnect Structures	Chapter 4	November 6
19	I/O Interconnect Structures	Chapter 4	November 8
20	I/O Devices	Chapter 9	November 11
Disc	*Memory Modeling Tools*		November 11
21	Memory	Chapter 5	November 13
22	Memory	Chapter 5	November 15
23	Memory	Chapter 5	November 18
Disc			November 18
24	Memory	Chapter 5	November 20
25	Memory	Chapter 5	November 22
26	Memory	Chapter 5	November 25
Disc			November 25
27	Memory	Chapter 5	November 27
28	Operating Systems	Chapter 6	December 2
Disc			December 2
29	Operating Systems	Chapter 6	December 4
30	Operating Systems	Chapter 6	December 6

Final: Monday, December 9, 8:00 A.M.