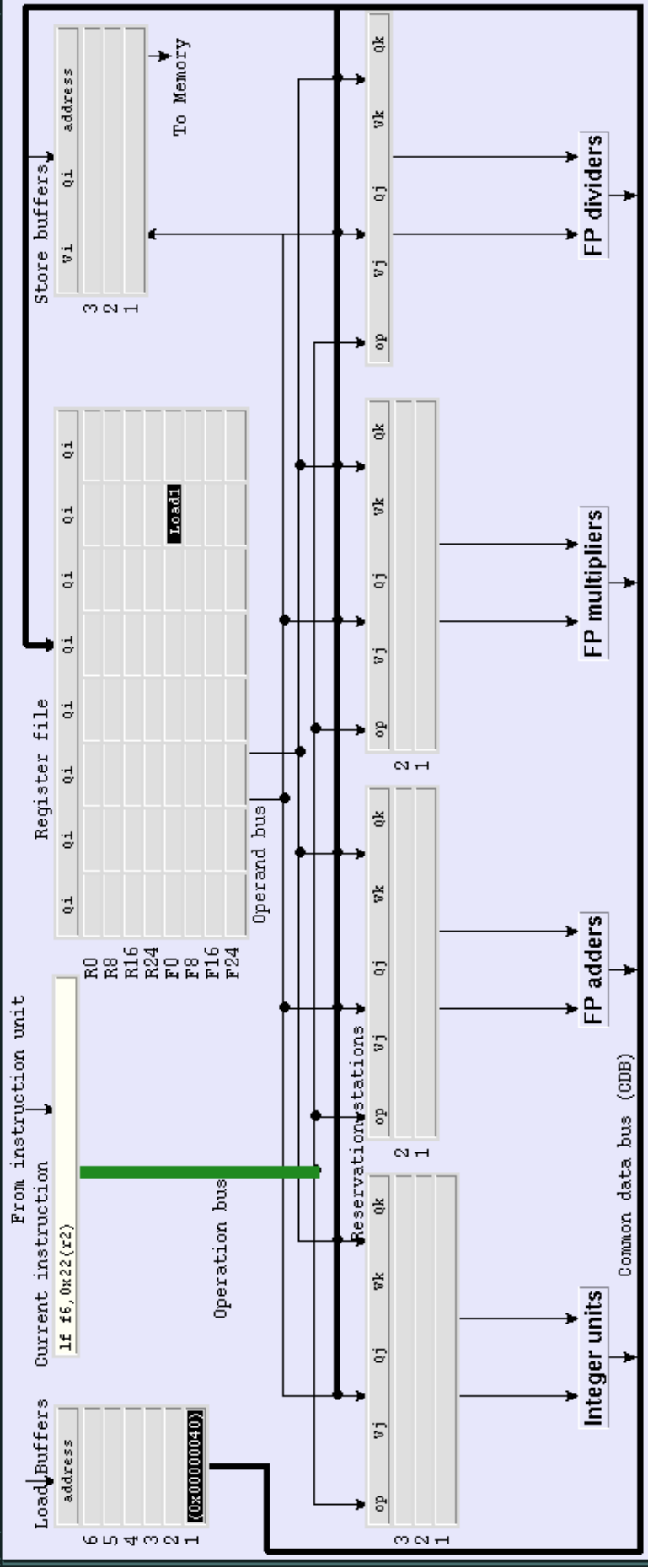


Tomasulo Algorithm on DLX

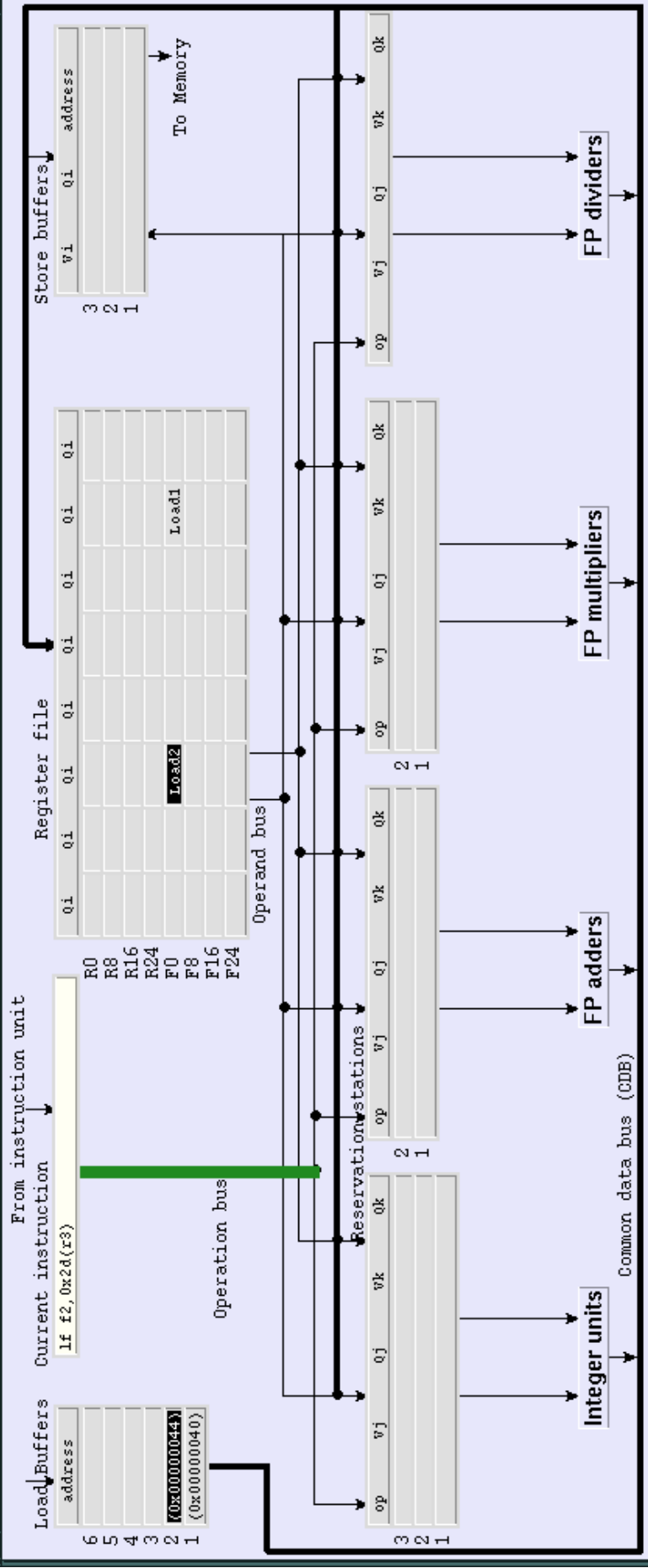


Instruction status			
Instruction	Issue	Execute	Write result
lf f6, 0x22(r2)	1	2	3

1

Issue Cycle of Current Instruction

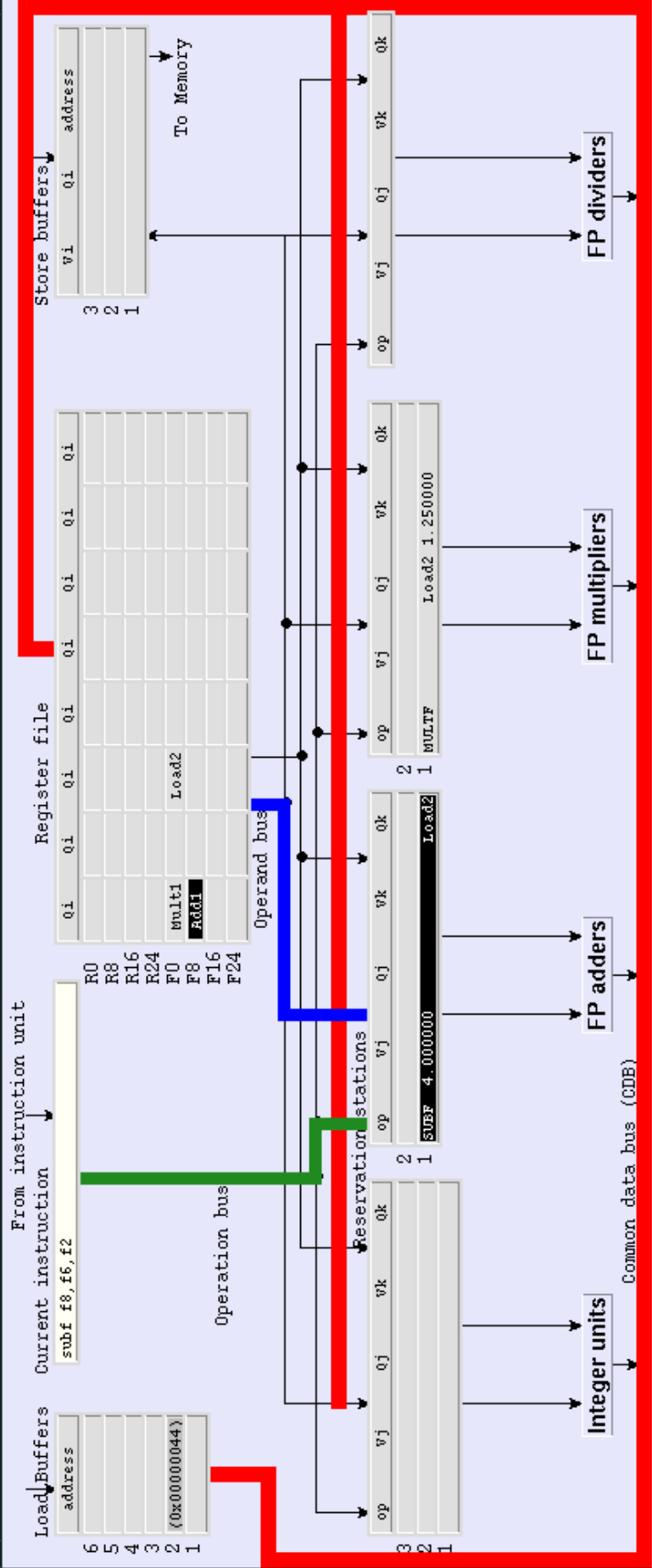
Tomasulo Algorithm on DLX



Instruction	Issue	Excute	Write result
<code>lf f6, 0x22(r2)</code>	1	2	3
<code>lf f2, 0x2d(r3)</code>	2	3	4

2

Tomasulo Algorithm on DLX

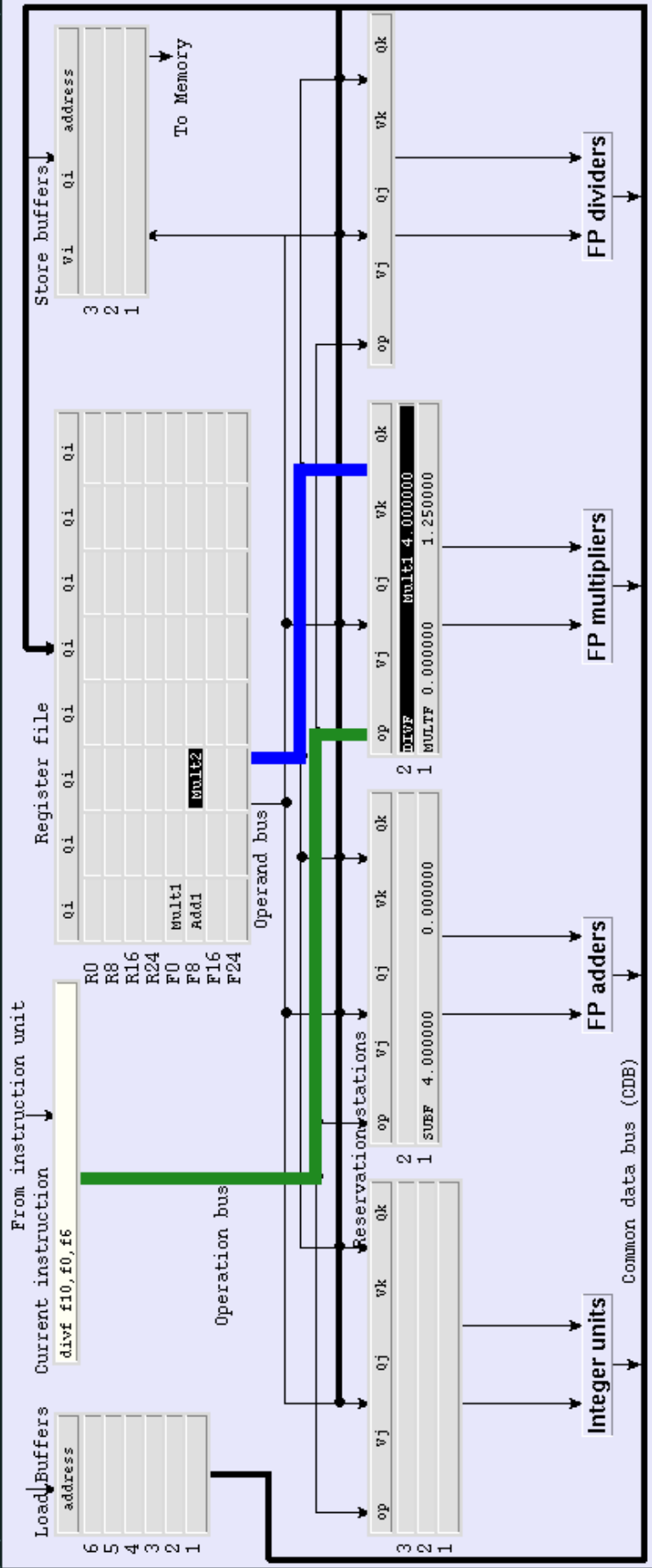


Instruction status				
Instruction	Issue	Excute	Write	result
1f f6, 0x22(r2)	1	2	3	
1f f2, 0x2d(r3)	2	3	4	
multf f0, f2, f4	3	5-9	10	
subf f8, f6, f2	4	5-6	7	

4

current cycle

Tomasulo Algorithm on DLX

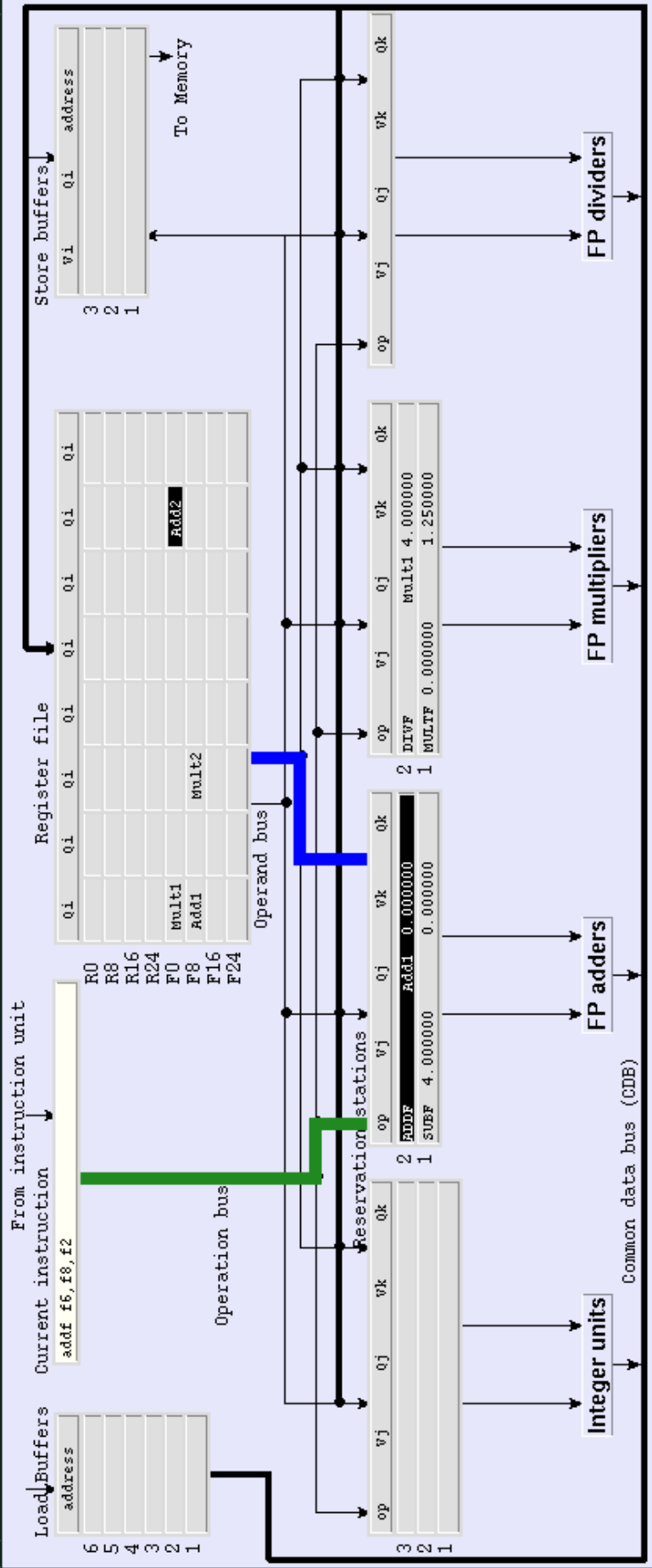


Instruction status				
Instruction	Issue	Excute	Write	result
lf f6, 0x22(r2)	1	2	3	
lf f2, 0x2d(r3)	2	3	4	
multf f0, f2, f4	3	5-9	10	
subf f8, f6, f2	4	5-6	7	
divf f10, f0, f6	5	11-29	30	

5

current cycle

Tomasulo Algorithm on DLX

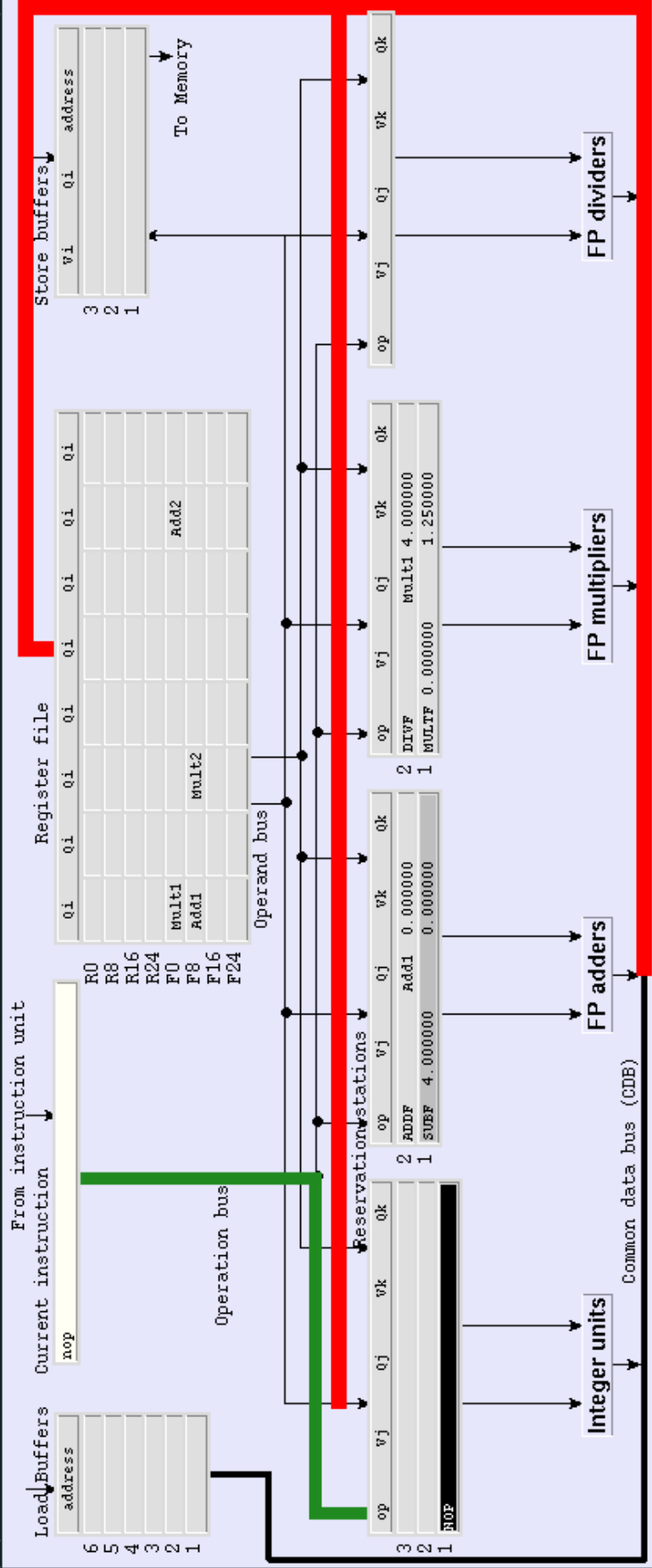


Instruction status				
Instruction	Issue	Execute	Write	result
lf f6, 0x22(r2)	1	2	3	
lf f2, 0x2d(r3)	2	3	4	
multf f0, f2, f4	3	5-9	10	
subf f8, f6, f2	4	5-6	7	
divf f10, f0, f6	5	11-29	30	
addf f6, f8, f2	6	8-10	11	

6

current cycle

Tomasulo Algorithm on DLX

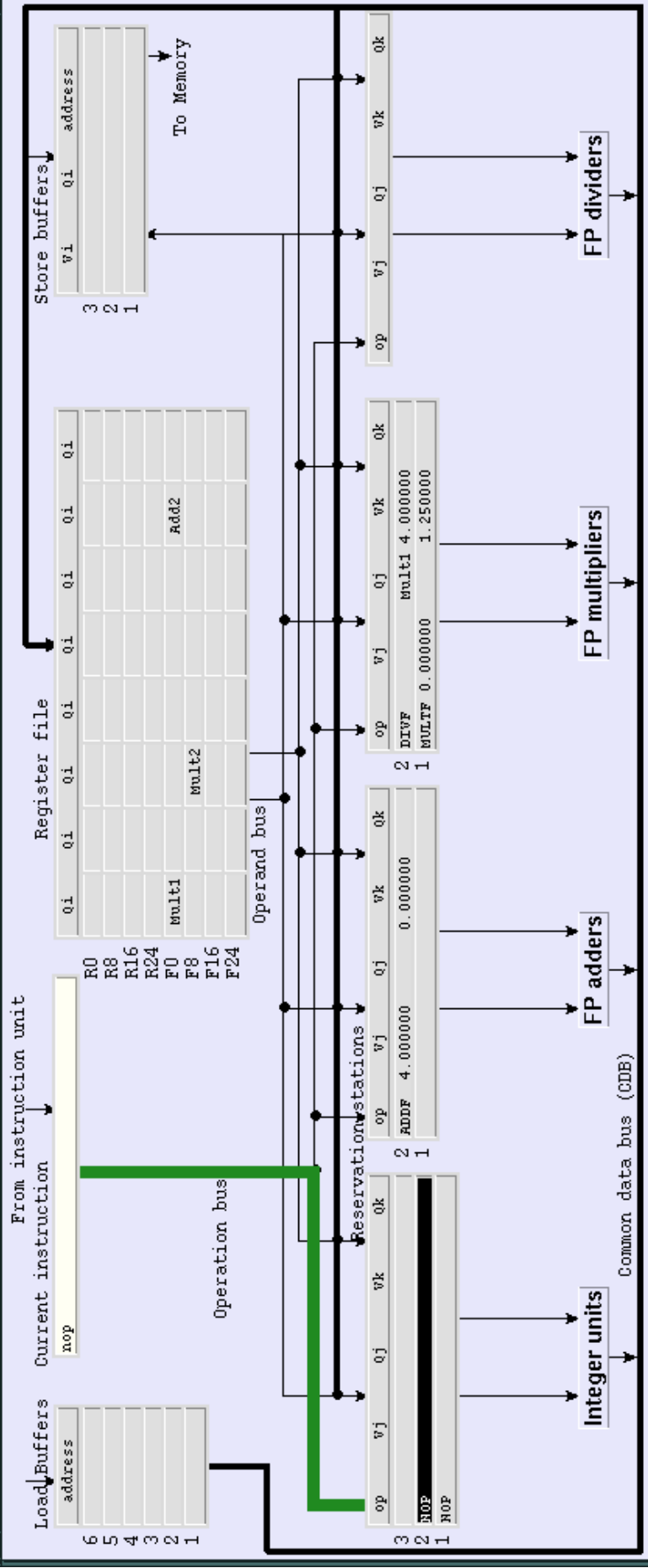


Instruction status				
Instruction	Issue	Execute	Write	result
lf f6, 0x22(r2)	1	2	3	
lf f2, 0x2d(r3)	2	3	4	
multf f0, f2, f4	3	5-9	10	
subf f8, f6, f2	4	5-6	7	
divf f10, f0, f6	5	11-29	30	
addf f6, f8, f2	6	8-10	11	
nop	7	8	9	

7

current cycle

Tomasulo Algorithm on DLX

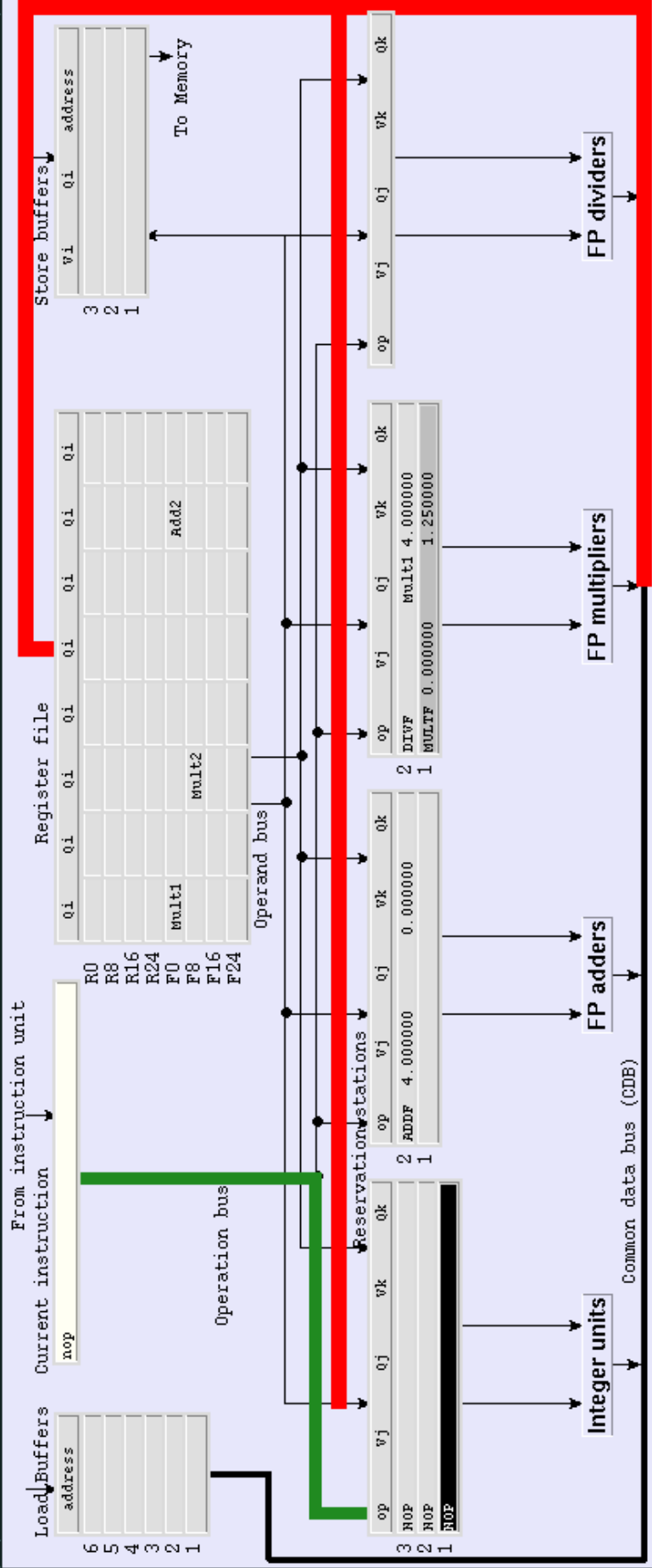


Instruction status				
Instruction	Issue	Excute	Write	result
lf f6, 0x22(r2)	1	2	3	
lf f2, 0x2d(r3)	2	3	4	
multf f0, f2, f4	3	5-9	10	
subf f8, f6, f2	4	5-6	7	
divf f10, f0, f6	5	11-29	30	
addf f6, f8, f2	6	8-10	11	
nop	7	8	9	
nop	8	9	10	

8

current cycle

Tomasulo Algorithm on DLX

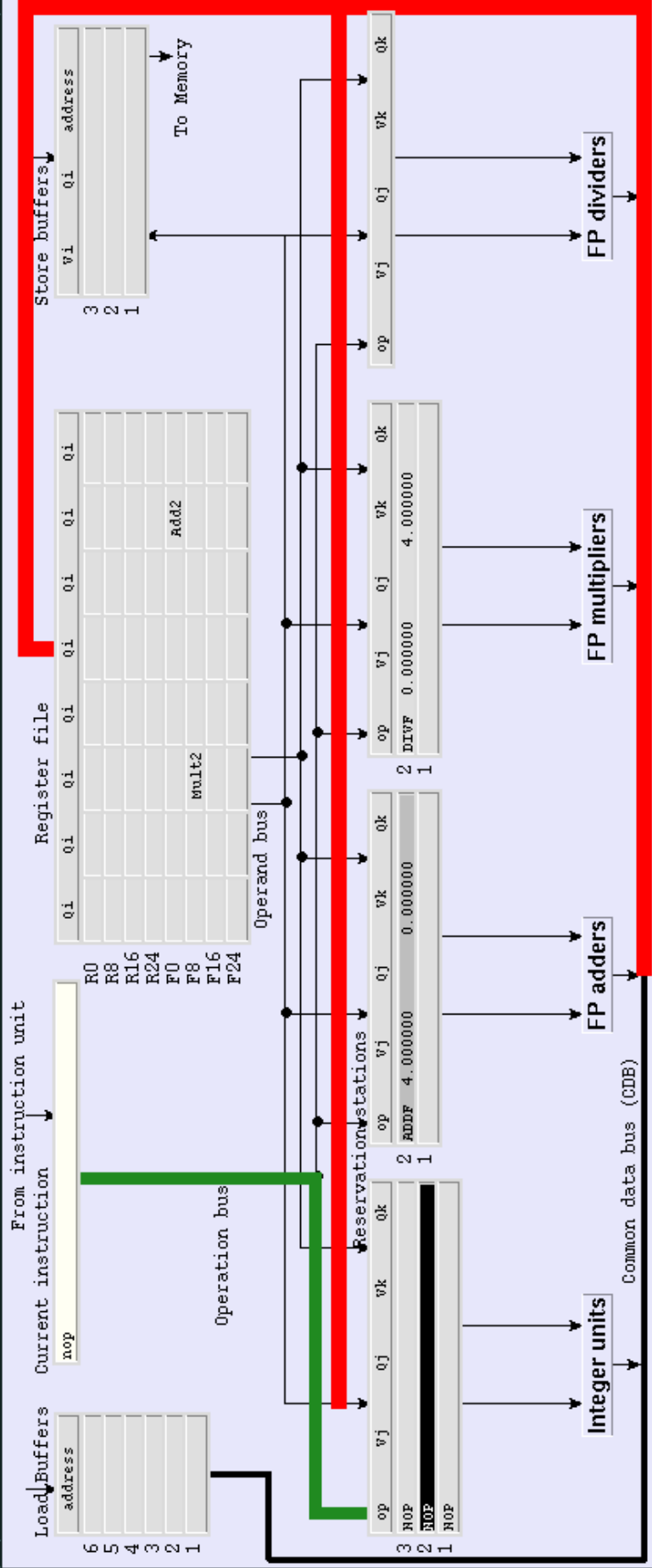


Instruction status			
Instruction	Issue	Excute	Write result
multf f0, f2, f4	3	5-9	10
subf f8, f6, f2	4	5-6	7
divf f10, f0, f6	5	11-29	30
addf f6, f8, f2	6	8-10	11
nop	7	8	9
nop	8	9	10
nop	9	10	11
nop	10	11	12

10

current cycle

Tomasulo Algorithm on DLX

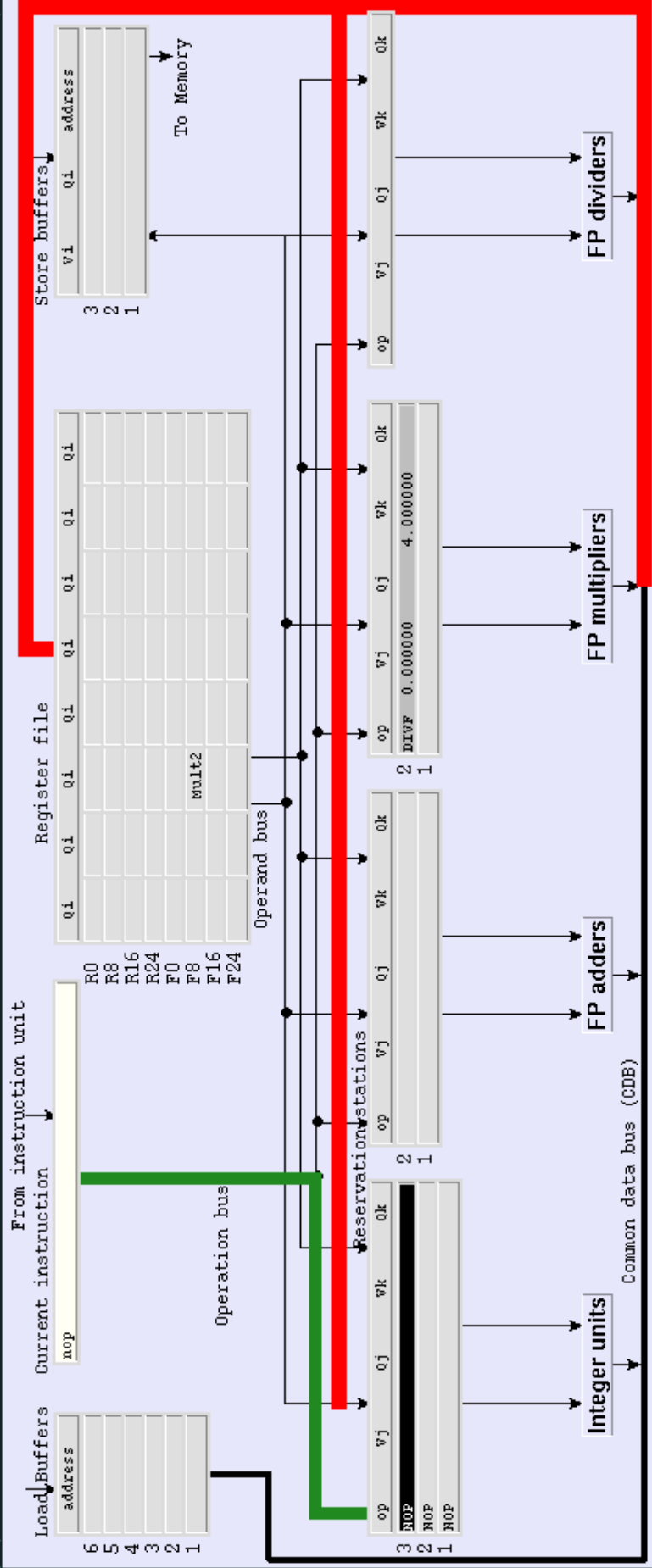


Instruction status				
Instruction	Issue	Excute	Write	result
subf f8, f6, f2	4	5-6	7	
divf f10, f0, f6	5	11-29	30	
addf f6, f8, f2	6	8-10	11	
nop	7	8	9	
nop	8	9	10	
nop	9	10	11	
nop	10	11	12	
nop	11	12	13	
nop	12	13	14	

II

current cycle

Tomasulo Algorithm on DLX



Instruction status			
Instruction	Issue	Execute	Write result
nop	23	24	25
nop	24	25	26
nop	25	26	27
nop	26	27	28
nop	27	28	29
nop	28	29	30
nop	29	30	31
nop	30	31	32

30

current cycle