

**Time = 1**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	<b>X</b>			
LF	F2,34(R3)				
MULTF	F0,F2,F4				
SUBF	F8,F6,F2				
DIVF	F10,F0,F6				
ADDF	F6,F8,F2				

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	<b>Yes</b>	<b>Load</b>	<b>Reg[F6]</b>	<b>Reg[R2]+32</b>		
Load2	Int/LS						
Add1	Add/Sub						
Add2	Add/Sub						
Add3	Add/Sub						
Mult1	Mult/Div						
Mult2	Mult/Div						

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	<b>Load1</b>								

**Time = 2**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	<b>X</b>		<b>X</b>	
LF	F2,34(R3)				
MULTF	F0,F2,F4				
SUBF	F8,F6,F2				
DIVF	F10,F0,F6				
ADDF	F6,F8,F2				

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	<b>Yes</b>	<b>Load</b>	<b>Reg[F6]</b>	<b>Reg[R2]+32</b>		
Load2	Int/LS						
Add1	Add/Sub						
Add2	Add/Sub						
Add3	Add/Sub						
Mult1	Mult/Div						
Mult2	Mult/Div						

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	<b>Load2</b>			Load1					

**Time = 3**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X		
LF	F2,34(R3)	X	X		
MULTF	F0,F2,F4	X			
SUBF	F8,F6,F2				
DIVF	F10,F0,F6				
ADDF	F6,F8,F2				

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	Yes	Load	Reg[F6]	Reg[R2]+32		
Load2	Int/LS	Yes	Load	Reg[F2]	Reg[R3]+34		
Add1	Add/Sub						
Add2	Add/Sub						
Add3	Add/Sub						
Mult1	Mult/Div	<b>Yes</b>	<b>Mult</b>	<b>0</b>	<b>Reg[F4]</b>	<b>Load2</b>	
Mult2	Mult/Div						

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	<b>Mult1</b>	Load2		Load1					

**Time = 4**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	
LF	F2,34(R3)	X	X		
MULTF	F0,F2,F4	X			
SUBF	F8,F6,F2	X			
DIVF	F10,F0,F6				
ADDF	F6,F8,F2				

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	Yes	Load	Reg[F6]	Reg[R2]+32		
Load2	Int/LS	Yes	Load	Reg[F2]	Reg[R3]+34		
Add1	Add/Sub	<b>Yes</b>	<b>Sub</b>	<b>0</b>	<b>0</b>	<b>Load1</b>	<b>Load2</b>
Add2	Add/Sub						
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	0	Reg[F4]	Load2	
Mult2	Mult/Div						

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	Mult1	Load2		Load1	<b>Add1</b>				

Time = 5

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	<b>Load1</b>
LF	F2,34(R3)	X	X	X	
MULTF	F0,F2,F4	X			
SUBF	F8,F6,F2	X			
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2				

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	Yes	Load	Reg[F6]	Reg[R2]+32		
Load2	Int/LS	Yes	Load	Reg[F2]	Reg[R3]+34		
Add1	Add/Sub	Yes	Sub	0	0	Load1	Load2
Add2	Add/Sub						
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	0	Reg[F4]	Load2	
Mult2	Mult/Div	<b>Yes</b>	<b>Div</b>	<b>0</b>	<b>0</b>	<b>Mult1</b>	<b>Load1</b>

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	Mult1	Load2		Load1	Add1	<b>Mult2</b>			

Time = 6

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	<b>Load2</b>
MULTF	F0,F2,F4	X			
SUBF	F8,F6,F2	X			
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X			

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	Yes	Load	Reg[F2]	Reg[R3]+34		
Add1	Add/Sub	Yes	Sub	Mem[]	0		Load2
Add2	Add/Sub	<b>Yes</b>	<b>Add</b>	<b>0</b>	<b>0</b>	<b>Add1</b>	<b>Load2</b>
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	0	Reg[F4]	Load2	
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)	Mult1	Load2		<b>Add2</b>	Add1	Mult2			

**Time = 7**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X		
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X			

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	Yes	Sub	Mem[]	Mem[]		
Add2	Add/Sub	Yes	Add	0	Mem[]	Add1	
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)		Mult1		Add2	Add1	Mult2			

**Time = 8**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X		
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X			

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	Yes	Sub	Mem[]	Mem[]		
Add2	Add/Sub	Yes	Add	0	Mem[]	Add1	
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)		Mult1		Add2	Add1	Mult2			

**Time = 9**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X	X	
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X			

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	Yes	Sub	Mem[]	Mem[]		
Add2	Add/Sub	Yes	Add	0	Mem[]	Add1	
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)		Mult1		Add2	Add1	Mult2			

**Time = 10**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X	X	<b>Add1</b>
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X			

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	Yes	Sub	Mem[]	Mem[]		
Add2	Add/Sub	Yes	Add	0	Mem[]	Add1	
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)		Mult1		Add2	Add1	Mult2			

**Time = 11**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X	X		

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	Yes	Add	(F6-F2)	Mem[]		
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									
	Mult1			Add2		Mult2			

**Time = 12**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X	X		

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	Yes	Add	(F6-F2)	Mem[]		
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									
	Mult1			Add2		Mult2			

**Time = 13**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X		
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X	X	X	

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	Yes	Add	(F6-F2)	Mem[]		
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									
	Mult1			Add2		Mult2			

**Time = 14**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X	X	
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X	X	X	<b>Add2</b>

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	Yes	Add	(F6-F2)	Mem[]		
Add3	Add/Sub						
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									
	Mult1			Add2		Mult2			

**Time = 15**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X	X	<b>Mult1</b>
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X			
ADDF	F6,F8,F2	X	X	X	X

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	No					
Add3	Add/Sub	No					
Mult1	Mult/Div	Yes	Mult	Mem[]	Reg[F4]		
Mult2	Mult/Div	Yes	Div	0	Mem[]	Mult1	

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									

**Time = 16**

Instruction Status					
Instruction		Issue Inst	Execute Underway	Execute Completes	Write Result
LF	F6,34(R2)	X	X	X	X
LF	F2,34(R3)	X	X	X	X
MULTF	F0,F2,F4	X	X	X	X
SUBF	F8,F6,F2	X	X	X	X
DIVF	F10,F0,F6	X	<b>X</b>		
ADDF	F6,F8,F2	X	X	X	X

Reservation Stations							
Tag	Name	Busy	Fm(op)	Val.j(Vj)	Val.k(Vk)	Tag.j(Qj)	Tag.k(Qk)
Load1	Int/LS	No					
Load2	Int/LS	No					
Add1	Add/Sub	No					
Add2	Add/Sub	No					
Add3	Add/Sub	No					
Mult1	Mult/Div	No					
Mult2	Mult/Div	Yes	Div	(F2*F4)	Mem[]		

Register Status									
	F0	F2	F4	F6	F8	F10	12	...	F30
Tag.i(Qi)									